

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: BUR920020055US1

In re Application of:

HAYES ET AL.

Serial No.: 10/604,107

Filed: 26 JUNE 2003

For: METHOD AND APPARATUS FOR
PERFORMING INPUT/OUTPUT FLOOR
PLANNING ON AN INTEGRATED
CIRCUIT DESIGN

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Examiner: PIERRE LOUIS, A.

Art Unit: 2123

AMENDMENT UNDER 37 C.F.R. § 1.116

MS AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

DON OT ENTER
7/27/06
APL

Sir:

The present Amendment is submitted in reply to the Examiner's Final Office Action dated May 18, 2006, having a shortened statutory period set to expire August 18, 2006.